

CompactFlash Bottom Connector (HOST/DEVICE)

The PPI is driven from the FPGA. The signal is also taken to the top connector so it can be driven externally.

- CF Bus
- A0
- A1
- A2
- A3
- A4
- A5
- A6
- A7
- A8
- A9
- A10
- REG
- MEM/IO
- SLOT 0
- SLOT 1
- SLOT 2

